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Forum: [8-bit PIC](#)

Topic: near [指令使用\(Access Bank\)](#)

Subject: Re: near [指令使用\(Access Bank\)](#)

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也請參考一下 MPASM 所提供各個元件組語的套用範例:

C:\Program Files (x86)\Microchip\MPASM Suite\TemplateObject

```
*****
;
;
;   This file is a basic code template for code generation on the      *
;   PIC18F45K20. This file contains the basic code building blocks to build *
;   upon.
;
;   Refer to the MPASM User's Guide for additional information on features  *
;   of the assembler.
;
;   Refer to the respective data sheet for additional information on the   *
;   instruction set.
;
*****
;
;   Filename:                xxx.asm
;   Date:
;   File Version:
;   Author:
;   Company:
;
*****
;
;   Files Required:
;
*****
;
;   Notes:
;
*****
;
;   Revision History:
;
*****
;
-----
; PROCESSOR DECLARATION
```

```

;-----
LIST          P=PIC18F45K20          ; list directive to define processor
#include <P18F45K20.INC>             ; processor specific variable definitions

;-----
;
;
; CONFIGURATION WORD SETUP
;
; The 'CONFIG' directive is used to embed the configuration word within the
; .asm file. The labels following the directive are located in the respective
; .inc file. See the data sheet for additional information on configuration
; word settings.
;
;-----

CONFIG FOSC = INTIO7, FCMEN = OFF, IESO = OFF, PWRT = OFF, BOREN = OFF
CONFIG BORV = 18, WDTEN = OFF, WDTPS = 1, MCLRE = ON, HFOFST = ON
CONFIG LPT10SC = OFF, PBAEN = OFF, CCP2MX = PORTC, STVREN = OFF
CONFIG LVP = OFF, XINST = OFF, CPO = OFF, CP1 = OFF, CP2 = OFF
CONFIG CP3 = OFF, CPB = OFF, CPD = OFF, WRT0 = OFF, WRT1 = OFF
CONFIG WRT2 = OFF, WRT3 = OFF, WRTB = OFF, WRTC = OFF, WRTD = OFF
CONFIG EBTR0 = OFF, EBTR1 = OFF, EBTR2 = OFF, EBTR3 = OFF
CONFIG EBTRB = OFF

;-----
;
;
; VARIABLE DEFINITIONS
;
;-----

CBLOCK 0x60 ; Sample GPR variable register allocations
MYVAR1     ; user variable at address 0x60
MYVAR2     ; user variable at address 0x61
MYVAR3     ; user variable at address 0x62
ENDC

W_TEMP      EQU          0x000    ; w register for context saving (ACCESS)
STATUS_TEMP EQU          0x001    ; status used for context saving
BSR_TEMP    EQU          0x002    ; bank select used for ISR context saving

;-----
; EEPROM INITIALIZATION
;
; The 18F45K20 has non-volatile EEPROM starting at 0xF00000
;
;-----

DATAEE      ORG          0xF00000 ; Starting address for EEPROM for 18F45K20

```

```
DE      "MCHP"      M' 'C' ;HPI à 0x0000 à address 0,1,2,3
```

```
-----  
; RESET VECTOR  
-----  
RES_VECT  ORG      0x0000      ; processor reset vector  
          GOTO     START      ; go to beginning of program
```

```
-----  
; HIGH PRIORITY INTERRUPT VECTOR  
-----
```

```
ISRH      ORG      0x0008  
  
          ; Run the High Priority Interrupt Service Routine  
          GOTO     HIGH_ISR
```

```
-----  
; LOW PRIORITY INTERRUPT VECTOR  
-----
```

```
ISRL      ORG      0x0018  
  
          ; Run the High Priority Interrupt Service Routine  
          GOTO     LOW_ISR
```

```
-----  
; HIGH PRIORITY INTERRUPT SERVICE ROUTINE  
-----
```

```
HIGH_ISR  
  
          ; Insert High Priority ISR Here  
  
          RETFIE  FAST
```

```
-----  
; LOW PRIORITY INTERRUPT SERVICE ROUTINE  
-----
```

```
LOW_ISR  
  
          ; Context Saving for Low ISR  
          MOVWF   W_TEMP      ; save W register  
          MOVFF   STATUS, STATUS_TEMP ; save status register  
          MOVFF   BSR, BSR_TEMP ; save bankselect register  
  
          ; Insert Low Priority ISR Here
```

```
    ; Context Saving for Low ISR
MOVFF    BSR_TEMP, BSR          ; restore bankselect register
MOV      W_TEMP, W              ; restore W register
MOVFF    STATUS_TEMP, STATUS ; restore status register
RETFIE
```

```
-----
; MAIN PROGRAM
-----
```

START

```
    ; Insert User Program Here

GOTO $          ; loop program counter

END
```