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[Topic: pic16f946使用crystal 32768做real time用之問題](#)

[Subject: Re: pic16f946使用crystal 32768做real time用之問題](#)

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引用:

Ryang 寫道:

如貼圖所示: LP OSC 的控制是使用 T10SCEN=1 and Fosc= 0x000 後才能開啟外部的 32768Hz 的震盪器。

T1CS = 1 也要設定。使用非同步模式, 這樣在睡眠模式下才可以計時。

所以 T1CON = 0b00001111 + Fosc= 0b000

bit 2-0 FOSC<2:0>: Oscillator Selection bits

111 = RC oscillator: CLK0 function on RA6/OSC2/CLK0/T10S0 pin, RC on RA7/OSC1/CLKI/T10SI

110 = RCIO oscillator: I/O function on RA6/OSC2/CLK0/T10S0 pin, RC on RA7/OSC1/CLKI/T10SI

101 = INTOSC oscillator: CLK0 function on RA6/OSC2/CLK0/T10S0 pin, I/O function on RA7/OSC1/CLKI/T10SI

100 = INTOSCIO oscillator: I/O function on RA6/OSC2/CLK0/T10S0 pin, I/O function on RA7/OSC1/CLKI/T10SI

011 = EC: I/O function on RA6/OSC2/CLK0/T10S0 pin, CLKI on RA7/OSC1/CLKI/T10SI

010 = HS oscillator: High-speed crystal/resonator on RA6/OSC2/CLK0/T10S0 and RA7/OSC1/CLKI/T10SI

001 = XT oscillator: Crystal/resonator on RA6/OSC2/CLK0/T10S0 and RA7/OSC1/CLKI/T10SI

000 = LP oscillator: Low-power crystal on RA6/OSC2/CLK0/T10S0 and RA7/OSC1/CLKI/T10SI

不對吧, 由圖中可看出Fosc=000或x00都可使能enable外部的crystal且用Fosc=000會使系統clock也跑在32.768k, 但若設Fosc=100系統時脈可以設定為 INTOSC可以跑得更快