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Forum: [8-bit PIC](#)

Topic: [PIC16F1615 HEF地址](#)

Subject: [Re: PIC16F1615 HEF地址](#)

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2019年12月04日 10:21:48

請參考下面Datasheet中HEF的範圍。

附加檔案:

[PIC16F1615_HEF.jpg](#)(179.90 KB)

3.1 Program Memory Organization

The enhanced mid-range core has a 15-bit program counter capable of addressing a 32K x 14 program memory space. [Table 3-1](#) shows the memory sizes implemented. Accessing a location above these boundaries will cause a wrap-around within the implemented memory space. The Reset vector is at 0000h and the interrupt vector is at 0004h (See [Figure 3-1](#)).

Device	Program Memory Space (Words)	Last Program Memory Address	High-Endurance Flash Memory Address Range ⁽¹⁾
PIC16(L)F1615/9	8,192	1FFFh	1F80h-1FFFh

Note 1: High-endurance Flash applies to low byte of each address in the range.