

您設計產品時的好朋友！



Forum: [8-bit PIC](#)

Topic: [PIC16F1619用MCC的I2C遇到問題](#)

Subject: [Re: PIC16F1619用MCC的I2C遇到問題](#)

作者: x19961204

2019年04月16日 13:48:35

看MCP4725的格式在第三個資料的時候ACK完就在一個STOP BIT
目前我是沒送那一個STOP BIT DAC可以WORK 但是會不會有什麼影響？

附加檔案:

03.JPG(101.53 KB)

MICROCHIP
Regional Training Centers

I2C1_Master_Write()

I2C™ 寫入函數的參數說明

```
void I2C1_MasterWrite(  
    uint8_t *pdata, // 寫入資料的陣列 (含位址)  
    uint8_t length, // 寫入資料的長度 (含位址)  
    uint16_t address, // Slave Device Address  
    I2C1_MESSAGE_STATUS *pstatus);
```

Diagram illustrating the I2C1 message structure:

- START
- Control Byte (S1010AAA0)
- Address High Byte (pdata[0])
- Address Low Byte (pdata[1])
- Data Byte 0 (pdata[2])
- Data Byte 31 (pdata[33])
- STOP

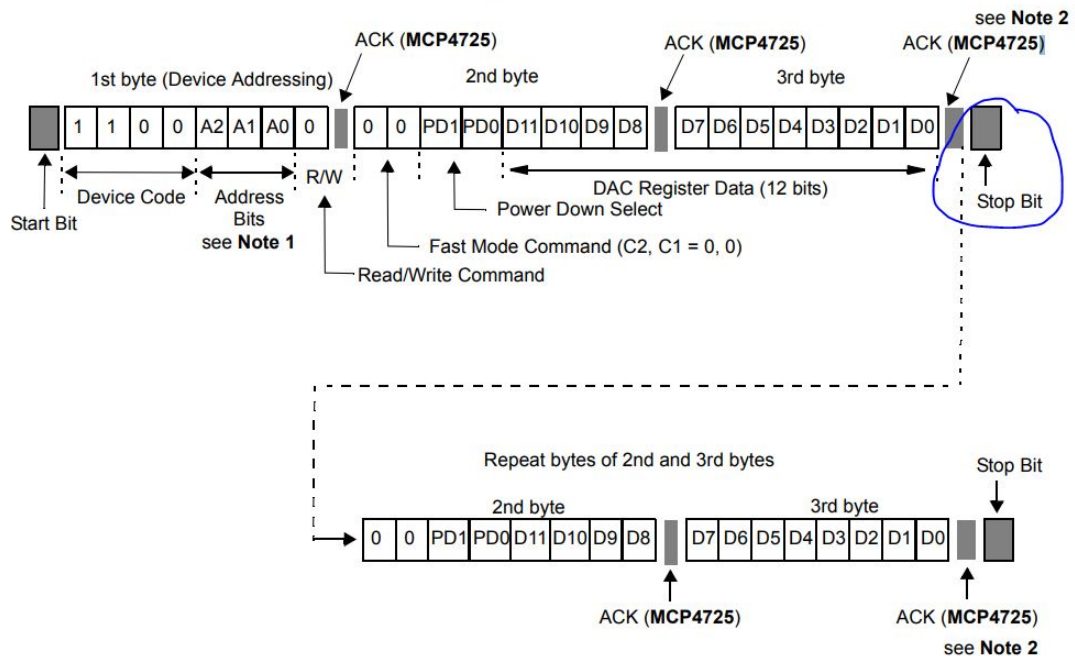
Handwritten notes: "STOP!" with an arrow pointing to the STOP bit.

24LC32A Page Write

© 2014 Microchip Technology Incorporated. All Rights Reserved. PIC18F MCC RTC_TW Slide 201

04.JPG(114.71 KB)

Change DAC Code in Fast Mode: (C2,C1) = (0,0)



Note 1: A2 and A1 bits are programmed at the factory by hard-wired, and A0 bit is determined by the logic state of A0 pin.

2: The device updates V_{OUT} at the falling edge of the ACK pulse of the 3rd byte.