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Topic: [MCC 產生之 I2C Slave 範例 code 請益](#)

Subject: [Re: MCC 產生之 I2C Slave 範例 code 請益](#)

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一般在 Data Sheet 不會有詳細的說明，請參考：

PIC32 Family Reference Manual, Sect. 24 Inter-Integrated Circuit

<http://ww1.microchip.com/downloads/en/DeviceDoc/61116F.pdf>

依聚嬰s氣的位元說明 I2COV: I2C Receive Overflow Status bit

1 =A byte is received while the I2CxRCV register is still holding the previous byte.

I2COV is a “don't care” in Transmit mode. This bit must be cleared in software.

0 = No overflow

在 Figure 24-30:Slave Message 有時序圖說明。此外也提及：

Reading the I2CxRCV register clears the RBF bit. The I2COV bit is cleared by writing to a '0' through software.