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[Topic: PIC48F47K40 指標取值問題](#)

[Subject: Re: PIC48F47K40 指標取值問題](#)

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找到問題了 真的是此系列IC 的BUG 勘誤表中提到

3. Module: PIC18 Core

3.1 TBLRD requires NVMREG value to point to appropriate memory

The affected silicon revisions of the PIC18FXXK40 devices improperly require the NVMREG<1:0> bits in the NVMCON register to be set for TBLRD access of the various memory regions. The issue is most apparent in compiled C programs when the user defines a const type and the compiler uses TBLRD instructions to retrieve the data from program Flash memory (PFM). The issue is also apparent when the user defines an array in RAM for which the compiler creates start-up code, executed before main(), that uses TBLRD instructions to initialize RAM from PFM.

Work around

Assembly code:

Set the NVMREG<1:0> bits to select the appropriate memory region before executing TBLRD instructions.

C code:

Create an assembly file named powerup.as and include this file with the other files in the project. This file will change the NVMREG<1:0> bits to point to program Flash before any code is executed.

Contents of the power-up.as file:

```
#include <xc.inc>
GLOBAL powerup, start
PSECT powerup, class=CODE, delta=1,
reloc=2
powerup:
BSF NVMCON1, 7
GOTO start
end
```

If there is a need to change the NVMREG<1:0> value to anything other than '10' and the Interrupt

Service Routine uses constants or literal strings,
then interrupts must be disabled before the
change and restored to '10' before interrupts are
enabled.

加上power-up.as 檔案 解決

累人的東西 不知道下次還會遇到設麼BUG
為甚麼明知有這問題 也不改善 這就是MICROCHIP