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Forum: [8-bit PIC](#)

Topic: PIC48F47K40 指標取值問題

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```
const char * Dpp1;
const char * Dpp2;

char      Data1;
char      Data2;

const char Word_10[10] =
{
0x46, 0xC2, 0x7F, 0x01, 0x33, 0x44, 0x80, 0x81, 0x43, 0xBA
};
void main(void)
{
    while (1)
    {
        Dpp1=Word_10;
        Data1=*Dpp1;

        Dpp2=Word_10[5];
        Data2=*Dpp2;
    }
}
```

理論上執行完 Data1=0X46 Data2=0x44 但我使用icd4 debug 看數據
Data1=0 Data2=0 ;

以下是program memory 內容

我取

```
Dpp1=Word_10;
Data1=*Dpp1;
```

部分

```
1,921 00F00 4600
1,922 00F02 7FC2
1,923 00F04 3301
1,924 00F06 8044
1,925 00F08 4381
```

```
2,002 00FA2 6E2E MOVWF Dpp1, ACCESS
2,003 00FA4 0E0F MOVLW 0xF
2,004 00FA6 6E2F MOVWF 0x2F, ACCESS
```

```

2,005 00FA8 502E   MOVF Dpp1, W, ACCESS
2,006 00FAA 102F   IORWF 0x2F, W, ACCESS
2,007 00FAC A4D8   BTFSS STATUS, 2, ACCESS
2,008 00FAE 0E00   MOVLW 0x0
2,009 00FB0 6E30   MOVWF 0x30, ACCESS
2,010 00FB2 C02E   MOVFF Dpp1, TBLPTR
2,011 00FB4 FFF6   NOP
2,012 00FB6 C02F   MOVFF 0x2F, TBLPTRH
2,013 00FB8 FFF7   NOP
2,014 00FBA C030   MOVFF 0x30, TBLPTRU
2,015 00FBC FFF8   NOP
2,016 00FBE 0008   TBLRD*
2,017 00FC0 CFF5   MOVFF TABLAT, Data1
2,018 00FC2 F032   NOP

```

看程序沒有甚麼問題

在執行TBLRD*前

TBLPTR =0x0F01

在執行完TBLRD*後 TABLAT 內的值 永遠是0x0

我已經想破頭了 用pic18f2320 測試這段程式讀出來的值是正常的

但是PIC48F47K40 卻不行

神奇的是 我用simulator 模擬去跑得到的值卻是對的

是不是我有甚麼東西沒設定到 導致無法讀取指標的值

我的configuration bit 如下

我有試過 更改EBTR0~7 沒用

誰能救救我

```

#pragma config FEXTOSC = OFF
#pragma config RSTOSC = HFINTOSC_64MHZ

```

```

#pragma config CLKOUTEN = OFF
#pragma config CSWEN = ON
#pragma config FCMEN = ON

```

```

// CONFIG2L
#pragma config MCLRE = EXTMCLR
#pragma config PWRTE = OFF
#pragma config LPBOREN = OFF
#pragma config BOREN = SBORDIS
// CONFIG2H
#pragma config BORV = VBOR_2P45
#pragma config ZCD = OFF
#pragma config PPS1WAY = ON
#pragma config STVREN = ON
#pragma config DEBUG = OFF

```

```
#pragma config XINST = OFF

// CONFIG3L
#pragma config WDTCPSS = WDTCPSS_31
#pragma config WDTE = OFF

// CONFIG3H
#pragma config WDTWSS = WDTWSS_7
#pragma config WDTCCS = SC

// CONFIG4L
#pragma config WRT0 = OFF
#pragma config WRT1 = OFF
#pragma config WRT2 = OFF
#pragma config WRT3 = OFF
#pragma config WRT4 = OFF
#pragma config WRT5 = OFF
#pragma config WRT6 = OFF
#pragma config WRT7 = OFF

// CONFIG4H
#pragma config WRTC = OFF
#pragma config WRTB = OFF
#pragma config WRTD = OFF
#pragma config SCANE = ON
#pragma config LVP = ON
// CONFIG5L
#pragma config CP = OFF
#pragma config CPD = OFF

// CONFIG5H

// CONFIG6L
#pragma config EBTR0 = OFF
#pragma config EBTR1 = OFF
#pragma config EBTR2 = OFF
#pragma config EBTR3 = OFF
#pragma config EBTR4 = OFF
#pragma config EBTR5 = OFF
#pragma config EBTR6 = OFF
#pragma config EBTR7 = OFF

// CONFIG6H
#pragma config EBTRB = OFF
```