

# 您設計產品時的好朋友！



[Forum: 16-bit PIC24/dsPIC](#)

[Topic: PIC24FJ256GB106 I2C使用問題](#)

[Subject: PIC24FJ256GB106 I2C使用問題](#)

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2017年12月27日 02:23:07

控制暫存器設定值是用MCC產生的

目前接腳沒有接IC 所以NACH是PIC24自己產生出來的

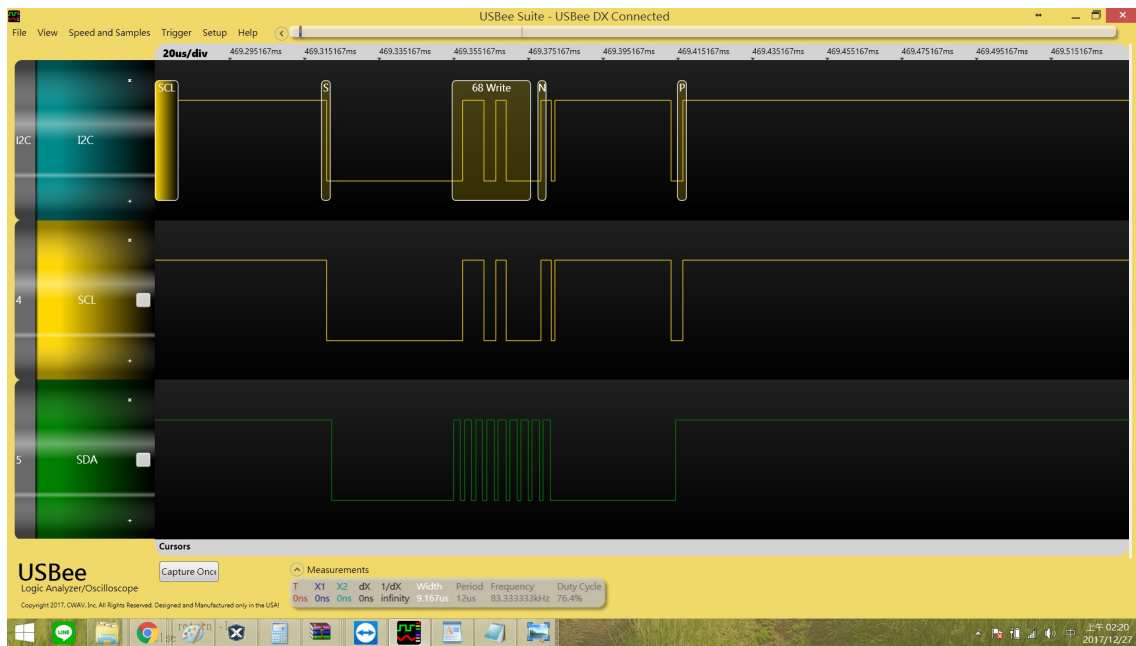
I2C 送完68就會產生一個NACK是怎麼一回事?

麻煩各位的幫忙一下 謝謝

附加檔案:

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螢幕擷取畫面 (9).jpg(263.60 KB)



螢幕擷取畫面 (10).jpg(446.99 KB)

MPLAB X IDE v3.45 - Lab\_ADC\_20170915 : default

File Edit View Navigate Source Refactor Run Debug Team Tools Window Help

Projects Files Services MPLAB Code Configurator main.c ...

Project Resources Generate

System Module

- Peripherals
  - OC1
  - TMR3
  - ADC1
  - I2C1
- Device Resources
  - Ext\_Interru
  - I2C
  - IC
  - OC
  - RTCC
  - SPT
  - Timer

Lab\_ADC - (main) - Vers -

Versions

- MPLAB Code Configurator v3.25
- Libraries
  - Microchip Technology, Inc.
    - Microcontrollers and Peripher
      - PIC10 / PIC12 / PIC16 / PIC18
      - PIC24 / dsPIC33 / PIC32MM
      - PIC32MX MCUs (v1.25)
    - Software
      - 8-bit Bootloader Library (v2)
      - LIN Library (v2.1)
      - mTouch Capacitive Sensing
      - TCP/IP Lite Stack (v2.1.0)
      - USB Framework Lite (v1.15)

```

85 //LDN_P=0x00; //0x00;
86
87
88
89
90 I2C1Write_SEN = 1;
91 while(I2C1Write_SEN);
92
93
94 I2C1TRW=0x68;
95 while(I2C1STATbits.TRSTAT);
96
97 //I2C1TRW=0x69; //I2C地址 + W
98
99 //while(I2C1STATbits.ACSTAT=1); //while(I2C1STATbits.ACSTAT=1);
100 // I2C1TRW=0x2C; //I2C地址 + R
101 //while(I2C1STATbits.ACSTAT=1);
102 I2C1Write_SEN=1; //I2C S
103 I2C1TRW=0x69; //I2C地址 + R
104 //while(I2C1STATbits.ACSTAT=1);
105 I2C1Write_SEN=1; //I2C R
106 I2C1Write_SEN=1; //I2C RACK
107 LDN_P=0x00; //I2C1TRW_3; //I2C1TRW_3;
108 I2C1Write_SEN=1; //I2C P
109
110 }
  
```

Pin Manager: Package (MCC)

MICROCHIP  
PIC24FJ256GB106

RES 1, RES2 2, RES3 3, RES4 4, RES5 5, RES6 6, RES7 7, RES8 8, RES9 9, RES10 10, RES11 11, RES12 12, RES13 13, RES14 14, RES15 15, RES16 16, RES17 17, RES18 18, RES19 19, RES20 20, RES21 21, RES22 22, RES23 23, RES24 24, RES25 25, RES26 26, RES27 27, RES28 28, RES29 29, RES30 30, RES31 31, RES32 32, RES33 33, RES34 34, RES35 35, RES36 36, RES37 37, RES38 38, RES39 39, RES40 40, RES41 41, RES42 42, RES43 43, RES44 44, RES45 45, RES46 46, RES47 47, RES48 48.

Output

Project Loading Warning MPLAB Code Configurator PICkit 3 Debugger Console Lab\_ADC\_20170915 (Build, Load, ...)

Programming...

The following memory area(s) will be programmed:  
program memory: start address = 0x0, end address = 0xffff  
configuration memory

Programming/Verify complete

94:13 IN5

上午 02:20  
2017/12/27