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Forum: [8-bit PIC](#)

Topic: PIC12F1840 RA3瞬間變身MCLR

Subject: Re: PIC12F1840 RA3瞬間變身MCLR

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PIC12F1840 有兩種燒錄模式:

1. 高壓 ICSP 燒錄模式:

- Hold ICSPCLK and ICSPDAT low. All other pins should be unpowered.
- Raise the voltage on MCLR from 0V to VIH.
- Raise the voltage on VDD FROM 0V to the desired operating voltage.

2. LVP ICSP 燒錄模式:

- using VDD only, without high voltage When the LVP bit of Configuration Word 2 register is set to '1'.

因為所有的燒錄及讀取及 Erase 等動作都是下串列訊號的命令 (如下圖所示), 所以 MCLR 接了高壓 7.5V 也不會清除裡面的資料的。

不過, 一個會產生異常的高壓的環境, 這本來就不好了, 想辦法將這高壓消掉吧! 這高壓的存在就是不正常的現象。

附加檔案:

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Command	Mapping		Data/Note
	Binary (MSb ... LSB)	Hex	
Load Configuration	x 0 0 0 0 0	00h	0, data (14), 0
Load Data For Program Memory	x 0 0 0 1 0	02h	0, data (14), 0
Load Data For Data Memory	x 0 0 0 1 1	03h	0, data (8), zero (6), 0
Read Data From Program Memory	x 0 0 1 0 0	04h	0, data (14), 0
Read Data From Data Memory	x 0 0 1 0 1	05h	0, data (8), zero (6), 0
Increment Address	x 0 0 1 1 0	06h	—
Reset Address	x 1 0 1 1 0	16h	—
Begin Internally Timed Programming	x 0 1 0 0 0	08h	—
Begin Externally Timed Programming	x 1 1 0 0 0	18h	—
End Externally Timed Programming	x 0 1 0 1 0	0Ah	—
Bulk Erase Program Memory	x 0 1 0 0 1	09h	Internally Timed
Bulk Erase Data Memory	x 0 1 0 1 1	0Bh	Internally Timed
Row Erase Program Memory	x 1 0 0 0 1	11h	Internally Timed

