Topic: MCP9808 Alert
Subject: Re: MCP9808 Alert
    : Jie
2017 04 20 16:40:38

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      Bit3, Alert Output Control Bit   Alert

   Alert Output Control Bit   TA < TUPPER Limit   TA < TCRITICAL Limit
          Alert   ?

        TA(26 C)   TUPPER Limit(70 C)   TCRITICAL Limit(80 C)   TLOWER
Limit(0 C)
                    TA vs. TCRIT bit   always High
          TA vs. TCRIT bit   always High?


```
void writeConfig()
{
    StartI2C2(); // send START command
    WriteMI2C2((MCP9808_I2CADDR_DEFAULT << 1) & 0xFE); //WRITE Command also, make sure
bit 0 is cleared ' O'
    WriteMI2C2(MCP9808_REG_CONFIG); // Write CONFIG Register
    WriteMI2C2(0x00);
    WriteMI2C2(0x08); // Alert Output Control Bit set 1
    StopI2C2(); // send STOP command
}
void writeTupper()
{
    StartI2C2(); // send START command
    WriteMI2C2((MCP9808_I2CADDR_DEFAULT << 1) & 0xFE); //WRITE Command also, make sure
bit 0 is cleared' O'
    WriteMI2C2(CP9808_REG_UPPER_TEMP); // Write CONFIG Register
    WriteMI2C2(0x04); // Write data 70 C
    WriteMI2C2(0x60); // Write data
    StopI2C2(); // send STOP command
}
void writeTlower()
{
    StartI2C2(); // send START command
    WriteMI2C2((MCP9808_I2CADDR_DEFAULT << 1) & 0xFE); //WRITE Command also, make sure
bit 0 is cleared ' O'
```

```
    WriteMI2C2(MCP9808_REG_LOWER_TEMP); // Write CONFIG Register
    WriteMI2C2(0x00); // Write data 0 C
    WriteMI2C2(0x00); // Write data
    StopI2C2(); // send STOP command
}
void writeTcrit()
{
    StartI2C2(); // send START command
    WriteMI2C2((MCP9808_I2CADDR_DEFAULT << 1) & 0xFE); //WRITE Command also, make sure
bit 0 is cleared ' 0'
    WriteMI2C2(MCP9808_REG_CRIT_TEMP); // Write CONFIG Register
    WriteMI2C2(0x05); // Write data 80 C
    WriteMI2C2(0x00); // Write data
    StopI2C2(); // send STOP command
}
```