



[Forum: 16-bit PIC/dsPIC \(請註明使用的元件標號\)](#)

Topic: MCP9808 Alert 問題

Subject: Re: MCP9808 Alert 問題

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CONFIGURATION Register

有一系列針對Alert Pin的動作設定，
其中Bit3, Alert Output Control Bit
預設是關閉的，可以打開看看

附加檔案:

01.jpg(253.46 KB)

- bit 4 **Alert Stat.:** Alert Output Status bit
0 = Alert output is not asserted by the device (power-up default)
1 = Alert output is asserted as a comparator/Interrupt or critical temperature output
This bit can not be set to '1' or cleared to '0' in Shutdown mode. However, if the Alert output is configured as Interrupt mode, and if the host controller clears to '0', the interrupt, using bit 5 while the device is in Shutdown mode, then this bit will also be cleared '0'.
- bit 3 **Alert Cnt.:** Alert Output Control bit
0 = Disabled (power-up default)
1 = Enabled
This bit can not be altered when either of the Lock bits are set (bit 6 and bit 7).
This bit can be programmed in Shutdown mode, but the Alert output will not assert or deassert.
- bit 2 **Alert Sel.:** Alert Output Select bit
0 = Alert output for T_{UPPER} , T_{LOWER} and T_{CRIT} (power-up default)
1 = $T_A > T_{CRIT}$ only (T_{UPPER} and T_{LOWER} temperature boundaries are disabled)
When the Alarm Window Lock bit is set, this bit cannot be altered until unlocked (bit 6).
This bit can be programmed in Shutdown mode, but the Alert output will not assert or deassert.
- bit 1 **Alert Pol.:** Alert Output Polarity bit
0 = Active-low (power-up default; pull-up resistor required)
1 = Active-high
This bit cannot be altered when either of the Lock bits are set (bit 6 and bit 7).
This bit can be programmed in Shutdown mode, but the Alert output will not assert or deassert.
- bit 0 **Alert Mod.:** Alert Output Mode bit
0 = Comparator output (power-up default)
1 = Interrupt output
This bit cannot be altered when either of the Lock bits are set (bit 6 and bit 7).
This bit can be programmed in Shutdown mode, but the Alert output will not assert or deassert.