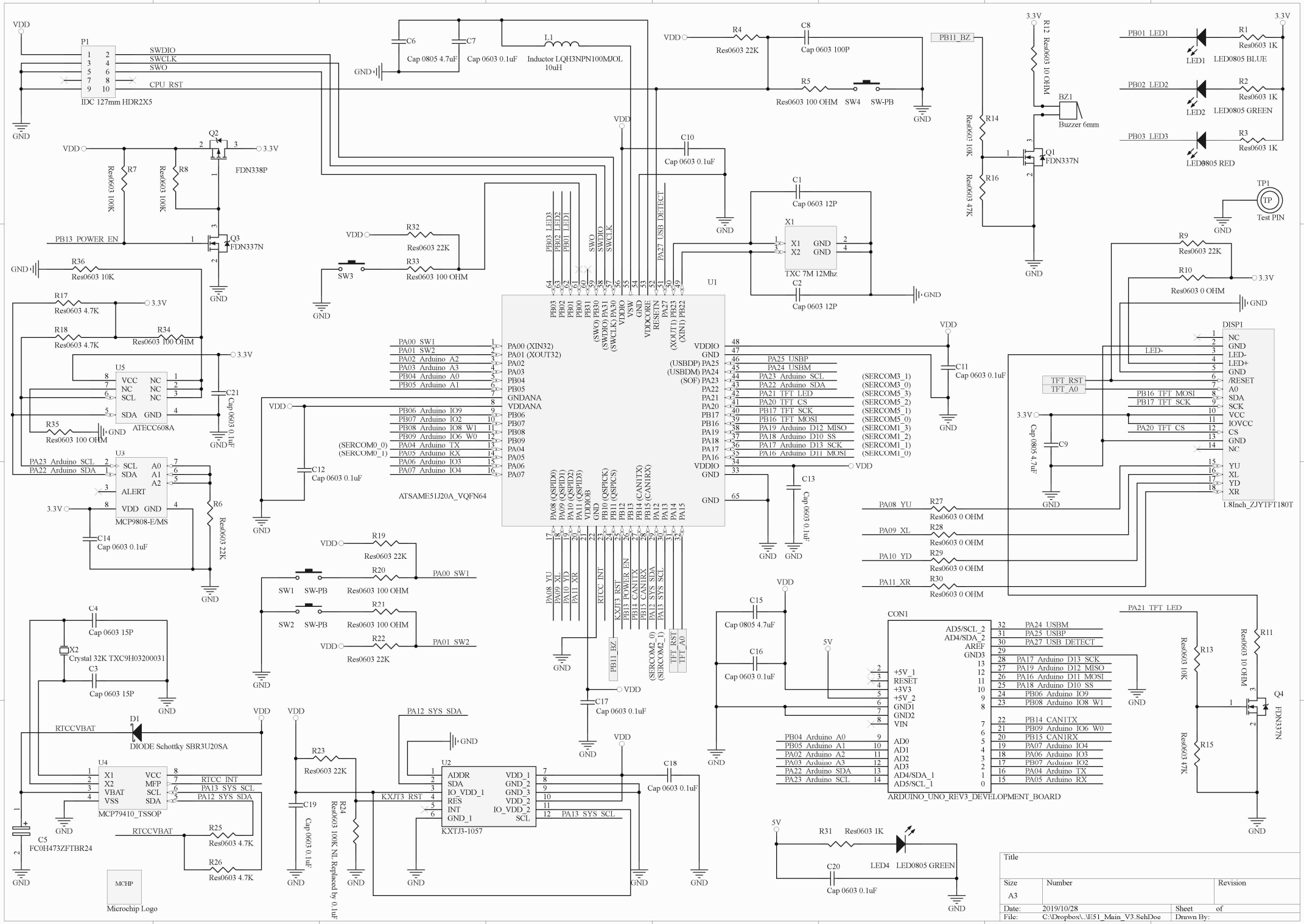
P1
IDC 127mm HDR2X5
SWDIO
SWCLK
SWO
CPU_RST
C6
Cap 0805 4.7uF
C7
Cap 0603 0.1uF
L1
Inductor LQH3NPN100MJOL 10uH
R4
Res0603 22K
C8
Cap 0603 100P
R5
Res0603 100 OHM
SW4
SW-PB
PB11_BZ
R12
Res0603 10 OHM
BZ1
Buzzer 6mm
R14
Res0603 10K
R16
Res0603 47K
Q1
FDN337N
PB01_LED1
R1
Res0603 1K
LED1
LED0805 BLUE
PB02_LED2
R2
LED2
LED0805 GREEN
PB03_LED3
R3
LED3
LED0805 RED
TP1
Test PIN
Q2
FDN338P
R7
Res0603 100K
R8
PB13_POWER_EN
Q3
R32
R33
SW3
C10
C1
Cap 0603 12P
X1
TXC 7M 12Mhz
C2
R9
Res0603 22K
R10
Res0603 0 OHM
R36
Res0603 10K
R17
Res0603 4.7K
R18
R34
Res0603 100 OHM
U5
ATECC608A
C21
R35
U3
MCP9808-E/MS
R6
C14
U1
ATSAME51J20A_VQFN64
C12
C11
C13
DISP1
1.8Inch_ZJYTFT180T
TFT_RST
TFT_A0
C9
Cap 0805 4.7uF
R27
R28
R29
R30
PA08_YU
PA09_XL
PA10_YD
PA11_XR
C4
Cap 0603 15P
X2
Crystal 32K TXC9H03200031
C3
R19
R20
R21
R22
SW1
SW2
C15
C16
CON1
ARDUINO_UNO_REV3_DEVELOPMENT_BOARD
D1
DIODE Schottky SBR3U20SA
RTCCVBAT
U4
MCP79410_TSSOP
C5
FC0H473ZFTBR24
R25
R26
R23
R24
Res0603 100K NI. Replaced by 0.1uF
C19
U2
KXTJ3-1057
C18
C17
R31
LED4
C20
Microchip Logo
Title
Size
A3
Number
Revision
Date: 2019/10/28
Sheet of
File: C:\Dropbox\..\E51_Main_V3.SchDoc
Drawn By: